BitWise Laboratories DDR5 Stress Accessory

Key Features

- Multi-function module to provide turn-key DDR5 solution
- Random and Sine jitter sources
- Sine interference source
- REFCLK source
- DDR5 VHOST interface
- Programmable delay, inversion, amplitude and DC-offset for REFCLK
- Extra Stressed Clock output
- Hot-pluggable ProbePower**TM** accessory for BitWise Laboratories Pattern Generator which provides user interface and automation control

Applications

- DDR5 Compliance Test
- DDR5 Design Validation and R&D
- General Purpose Stress for Pattern Generator

BitWise Laboratories DDR5 Stress Accessory operates in conjunction with the BitWise Laboratories Pattern Generator (PPG2CHA) to provide a turn-key solution for DDR5 testing. The module provides Random Jitter (RJ) and Sine Jitter (SJ), as well as Sine Interference (SI) that is externally summed, to provide timing and amplitude stresses for Stressed Eye testing. The module also provides DDR5 REFCLK signals, and a VHOST interface that enables DUT control.

The DDR5 Stress Accessory receives power and control over its ProbePower**TM** interface by connecting it to a Pattern Generator which also provides the full range of non-stressed clock signals appropriate for DDR5 testing.

The DDR5 Stress Accessory is used in conjunction with a Pattern Generator (PPG2CHA)

Once the ProbePower**TM** *connection is made to the DDR5 Stress Accessory, the host automatically recognize it, retrieves its calibration information, and incorporates it into the analyzer's user interface and automation control*

Through this accessory, the Pattern Generator can program DDR5 Modules into loopback on any lane or phase and can configure Decision Feedback Equalization parameters.

User interface to DDR5 Stress Accessory features is provided by the host analyzer.

Performance (PRELIMINARY)

REFCLK Output	*50 Ohm, AC/DC coupled, SMA Female, Differential, 150 mV to 500 mV PP SE, -2 V to +3.3 V Offset, Programmable inversion, 0 to 150 psec Delay insertion*
STRESS CLK Output	*50 Ohm, AC coupled, SMA Female, Differential, 150 mV to 800 mV PP SE, RJ fixed 10 MHz to 1 GHz band, up to 200 psec PP, SJ single frequency selectable from 10 KHz to 200 MHz, up to 200 psec PP*
Sine Interference Output	*50 Ohm, AC coupled, SMA Female, Single ended, single frequency selectable from 500 to 3000 MHz with 100 Khz resolution, up to 700 mV PP, for use with an external RF summing device*
VHOST Interface	*10-pin Locking Connector, I2C bus + 6 LOGIC outputs*
ProbePower™ Interface	*8-pin locking connector, control and power up to 12 watts*
Power	*6.4 watt*
Dimensions	*2.25" (L) x 6" (W) x 0.8" (H)*

Warranty

Products from BitWise Laboratories. come with a one year limited warranty. BitWise Laboratories will repair or, at its option, replace any defective product returned to BitWise Laboratories within one year of the date of purchase. This warranty applies to defects that are not due to misuse, neglect, accident or by abnormal operating conditions. Contact us for return material authorization. An additional 2-year warranty extension is available at the time of purchase.

DDR5 Stress Accessory bottom view

Ordering Information

DDR5SA	**DDR5 Stress Accessory**
PPG2CHA	**Pattern Generator**
WARRANTY2YR	**Additional 2 year warranty**

Send information requests to: sales@bitwiselabs.com

Company

BitWise Laboratories is located in California USA in the heart of Silicon Valley. Our founders have spent decades in Test & Measurement specializing in creating innovative tools that are easy to use and provide more diagnostic information for development and test engineering. The BitWise Laboratories line of products builds on this legacy and provides RF and PAM4 tools for today's communications engineering challenges. We provide compact and economical instruments that utilize world-class SiGe technology to achieve uncompromising performance. We provide innovative software with convenient multi-user web browser user interfaces for viewing and easy data download with complete remote control automation.   And we listen to our customers to help us define the next generation of features that will make our tools even better.